

FIGURE 1  
100

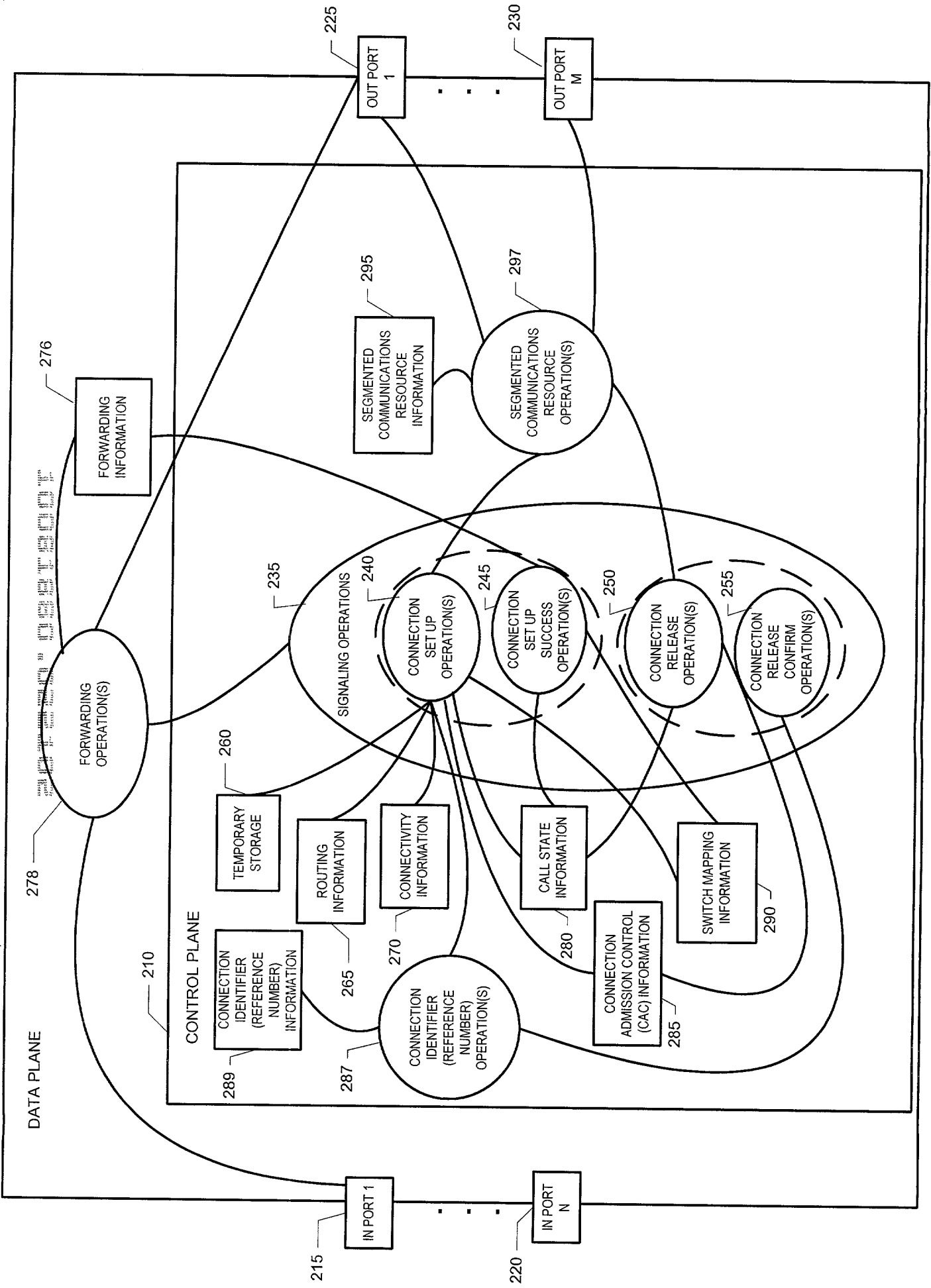


FIGURE 2

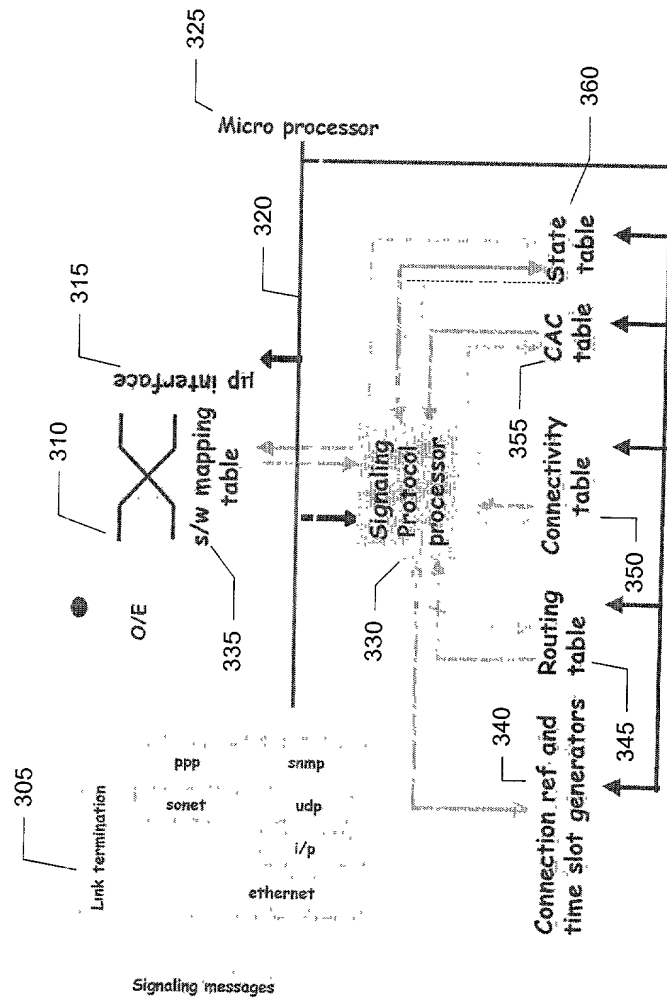


FIGURE 3

FIGURE 4

Setup Message (0001) 400

Message Length	Destination IP Address	Msg. Typ	Connection Reference (previous node)
Source IP Address			
Previous Node's IP Address			
Minimum Bandwidth	Maximum Bandwidth	Interface Number	Time-Slot Number
Pad Bits	Checksum		

FIGURE 5

Setup Success Message (0010) 500

Message Length	Bandwidth	Msg. Typ	Connection Reference (sender)
Connection Reference (own)	Pad Bits	Checksum	

FIGURE 6

Release (0011) and Release Confirm (0100) Message 600

Message Length	Cause	Msg. Typ	Connection Reference (sender)
Connection Reference (own)	Pad Bits	Checksum	

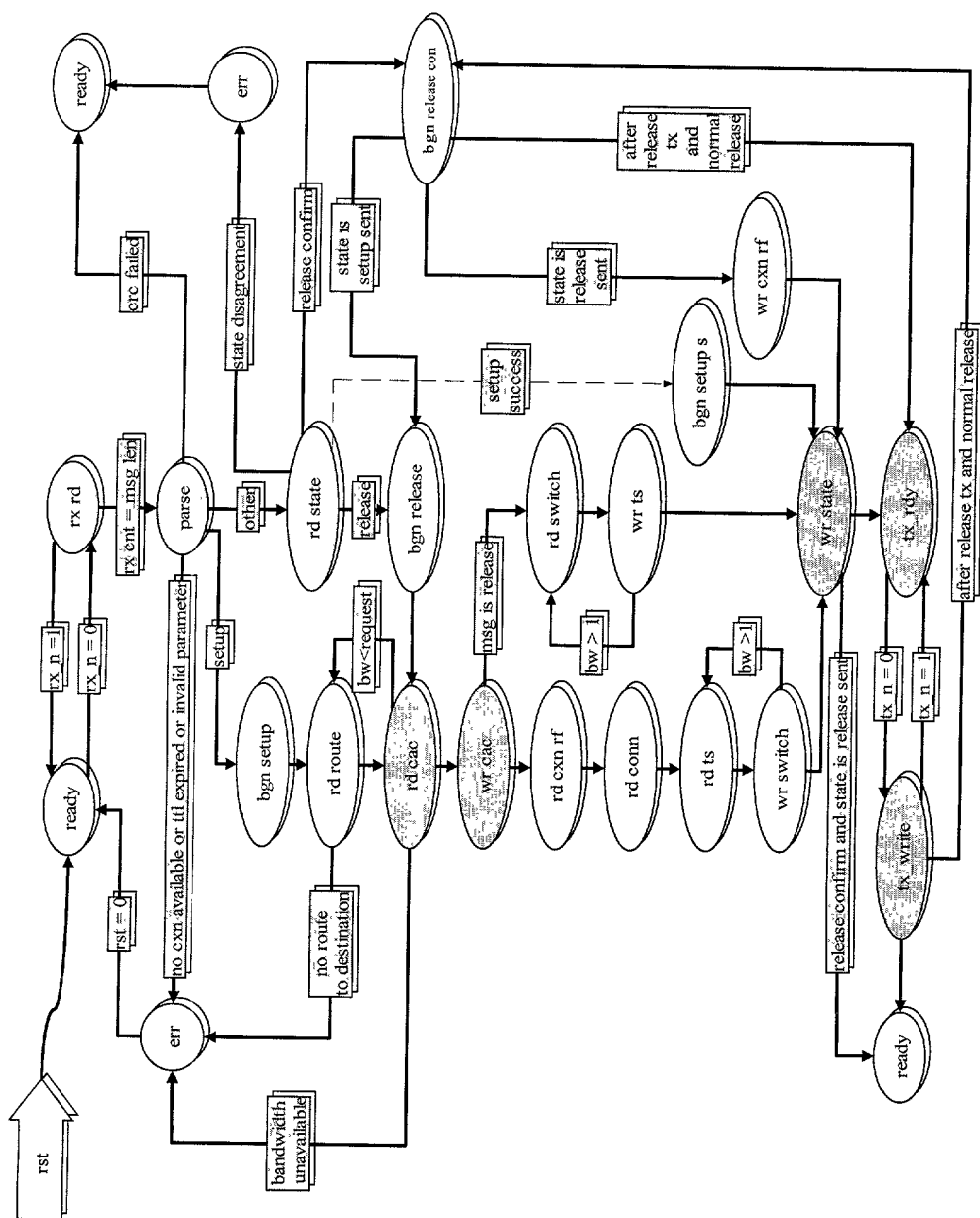


FIGURE 7

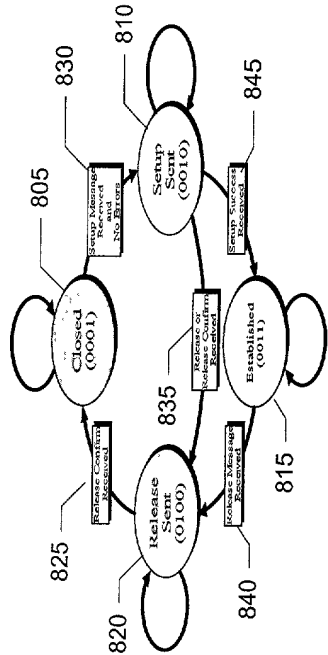


FIGURE 8

## Switch Mapping Table

[illegible]

State Table 1000

State Table 1000									
INDEX		RETURN / WRITTEN VALUE							
OWN CONNECTION REFERENCE	CONNECTION REFERENCE		STATE	BANDWIDTH	NODE ADDRESS		OWN CONNECTION REFERENCE		
	PREVIOUS	NEXT			PREVIOUS	NEXT	PREVIOUS	NEXT	

## Connectivity Table 1100

Connectivity Table 1100		
INDEX		
NEIGHBOR ADDRESS	NEIGHBOR INTERFACE #	RETURN VALUE OWN INTERFACE#

### Routing Table 1200

Routing Table 1200			
INDEX			
DESTINATION ADDRESS	REQUESTED BANDWIDTH	OPTION #	RETURN VALUE NEXT NODE ADDRESS
00000000	00000000	00000000	00000000
00000001	00000000	00000000	00000000
00000002	00000000	00000000	00000000
00000003	00000000	00000000	00000000
00000004	00000000	00000000	00000000
00000005	00000000	00000000	00000000
00000006	00000000	00000000	00000000
00000007	00000000	00000000	00000000
00000008	00000000	00000000	00000000
00000009	00000000	00000000	00000000
0000000A	00000000	00000000	00000000
0000000B	00000000	00000000	00000000
0000000C	00000000	00000000	00000000
0000000D	00000000	00000000	00000000
0000000E	00000000	00000000	00000000
0000000F	00000000	00000000	00000000
00000010	00000000	00000000	00000000
00000011	00000000	00000000	00000000
00000012	00000000	00000000	00000000
00000013	00000000	00000000	00000000
00000014	00000000	00000000	00000000
00000015	00000000	00000000	00000000
00000016	00000000	00000000	00000000
00000017	00000000	00000000	00000000
00000018	00000000	00000000	00000000
00000019	00000000	00000000	00000000
0000001A	00000000	00000000	00000000
0000001B	00000000	00000000	00000000
0000001C	00000000	00000000	00000000
0000001D	00000000	00000000	00000000
0000001E	00000000	00000000	00000000
0000001F	00000000	00000000	00000000
00000020	00000000	00000000	00000000
00000021	00000000	00000000	00000000
00000022	00000000	00000000	00000000
00000023	00000000	00000000	00000000
00000024	00000000	00000000	00000000
00000025	00000000	00000000	00000000
00000026	00000000	00000000	00000000
00000027	00000000	00000000	00000000
00000028	00000000	00000000	00000000
00000029	00000000	00000000	00000000
0000002A	00000000	00000000	00000000
0000002B	00000000	00000000	00000000
0000002C	00000000	00000000	00000000
0000002D	00000000	00000000	00000000
0000002E	00000000	00000000	00000000
0000002F	00000000	00000000	00000000
00000030	00000000	00000000	00000000
00000031	00000000	00000000	00000000
00000032	00000000	00000000	00000000
00000033	00000000	00000000	00000000
00000034	00000000	00000000	00000000
00000035	00000000	00000000	00000000
00000036	00000000	00000000	00000000
00000037	00000000	00000000	00000000
00000038	00000000	00000000	00000000
00000039	00000000	00000000	00000000
0000003A	00000000	00000000	00000000
0000003B	00000000	00000000	00000000
0000003C	00000000	00000000	00000000
0000003D	00000000	00000000	00000000
0000003E	00000000	00000000	00000000
0000003F	00000000	00000000	00000000

[illegible]

Connection Admission Control (CAC) Table 1300	
INDEX	RETURN / WRITTEN VALUE
NEXT NODE ADDRESS	NEXT NODE INTERFACE#
	TOTAL BANDWIDTH
	AVAILABLE BANDWIDTH

## FIGURE 9

FIGURE 10

FIGURE 11

FIGURE 12

FIGURE 13

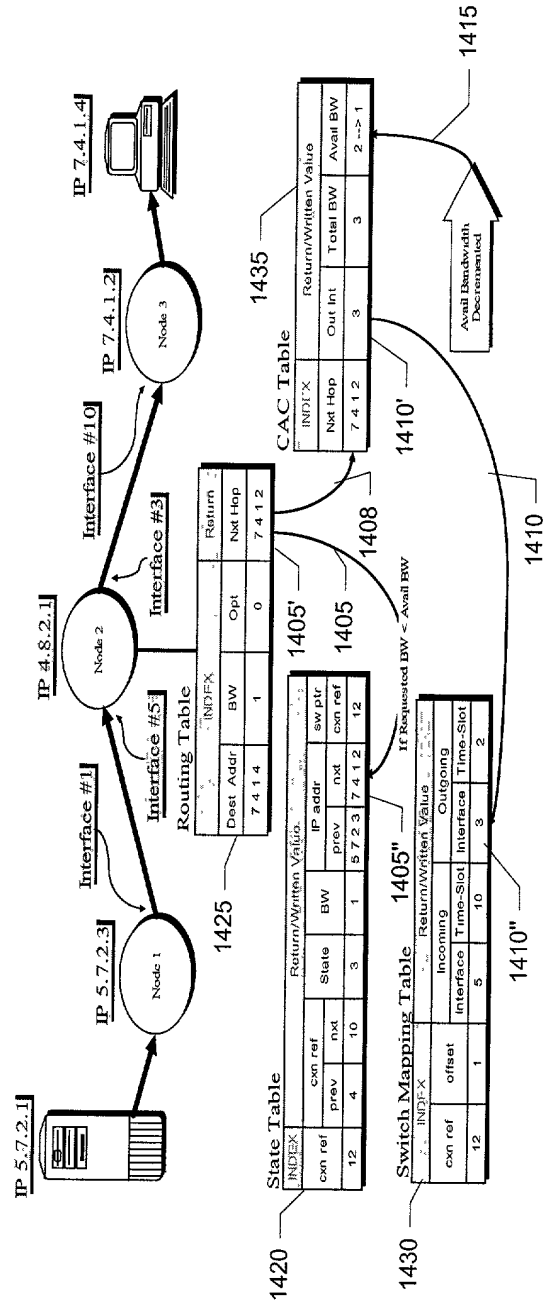


FIGURE 14



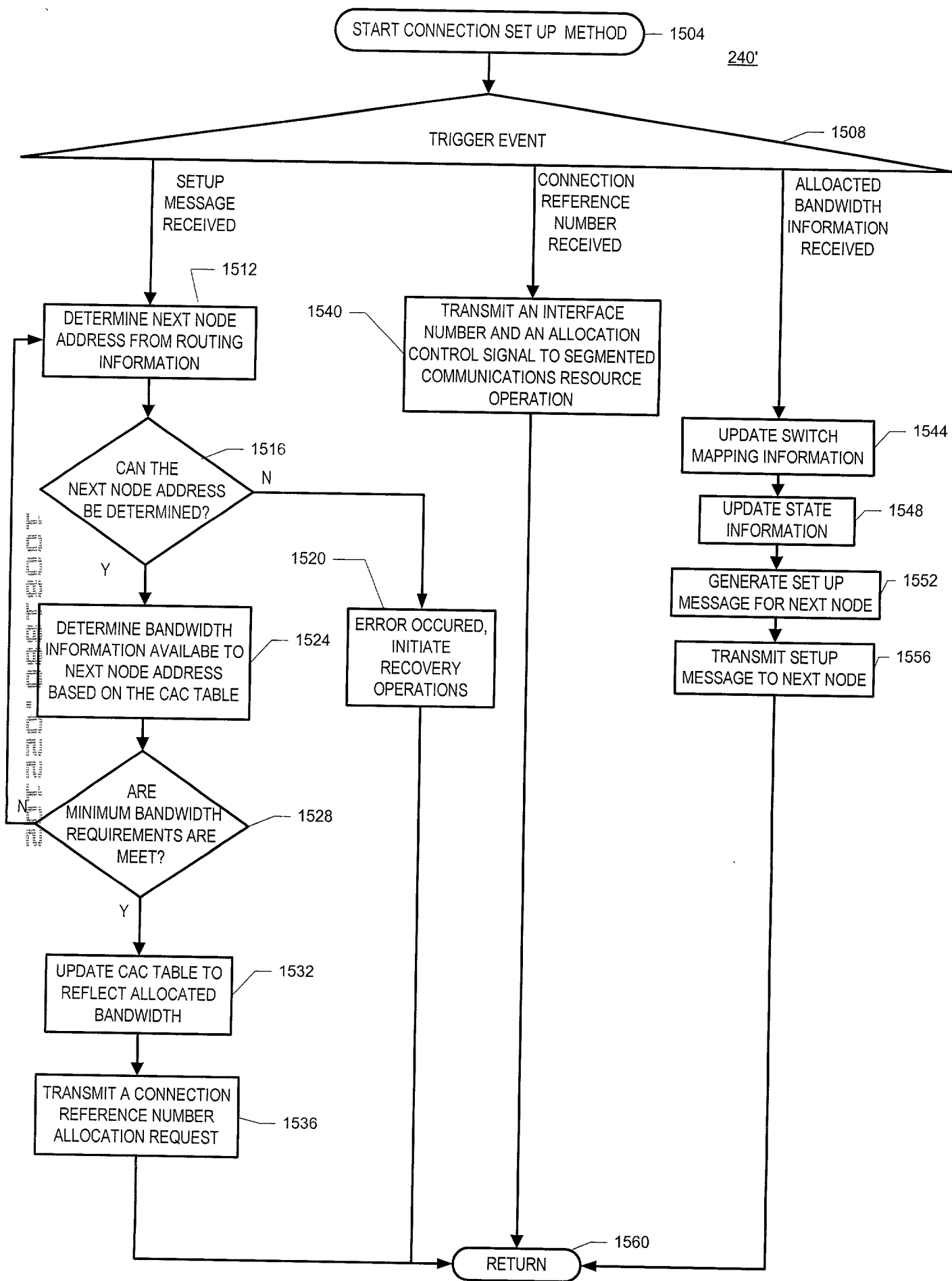


FIGURE 15

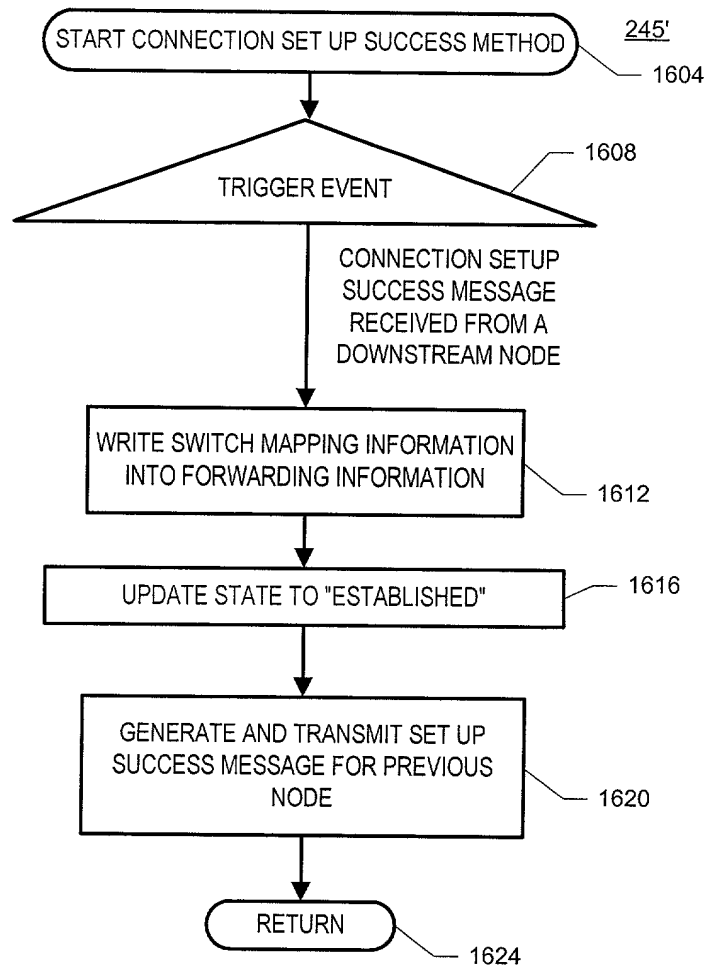


FIGURE 16

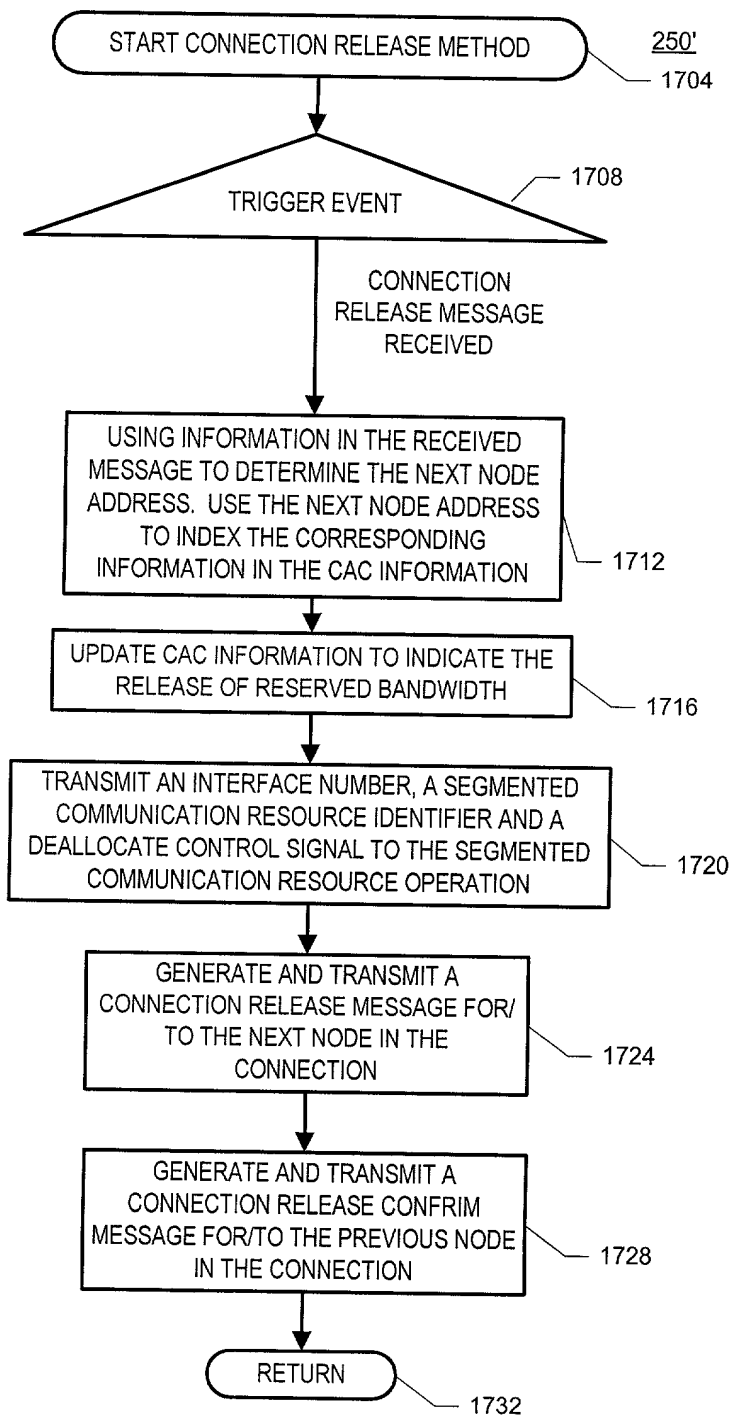


FIGURE 17

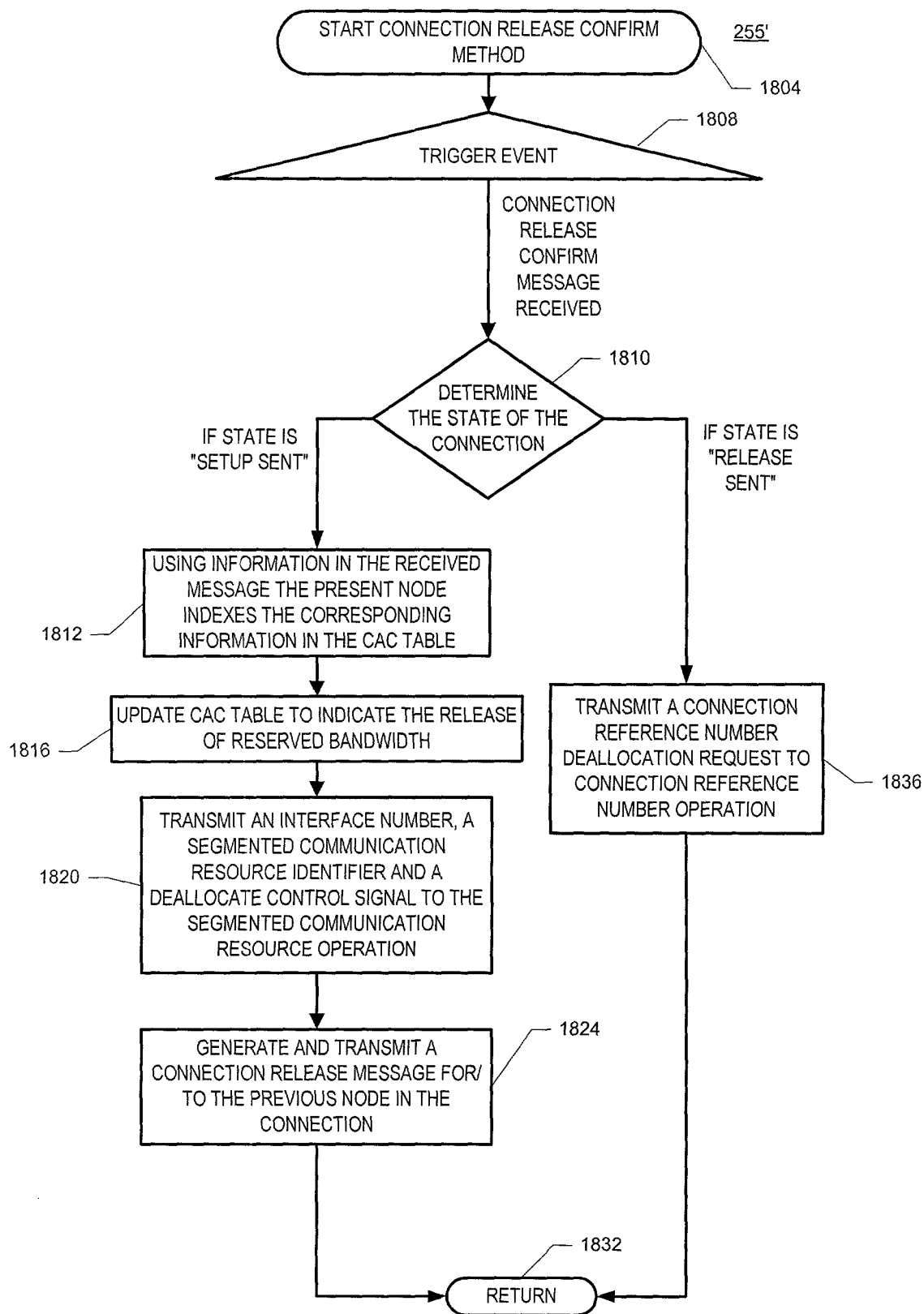


FIGURE 18

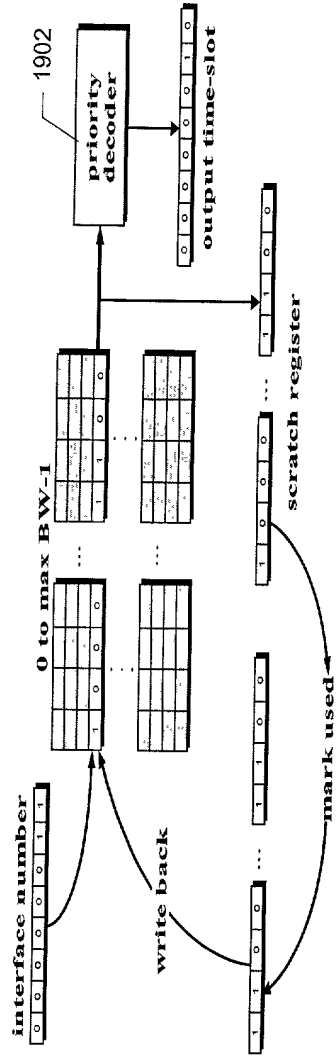


FIGURE 19

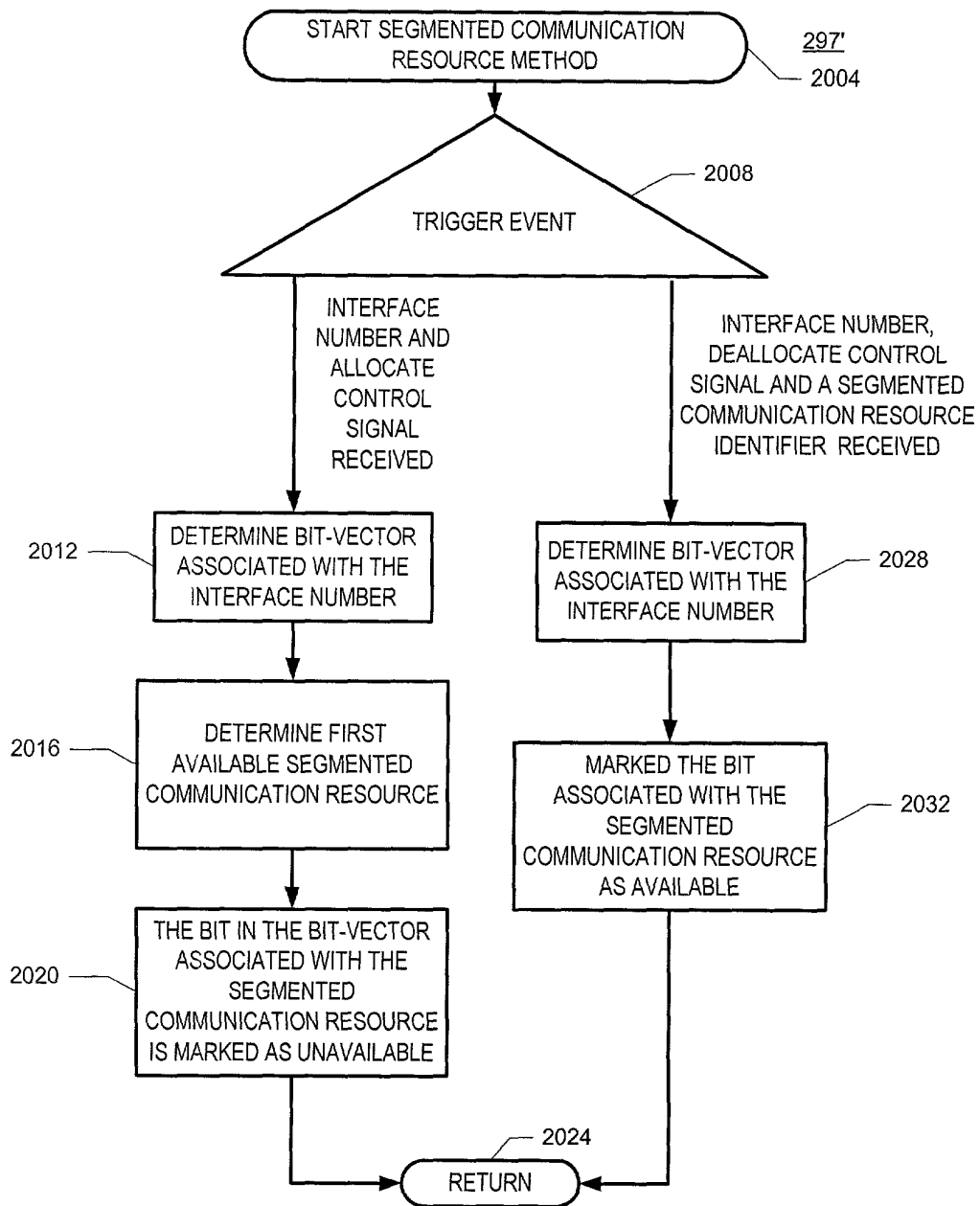


FIGURE 20

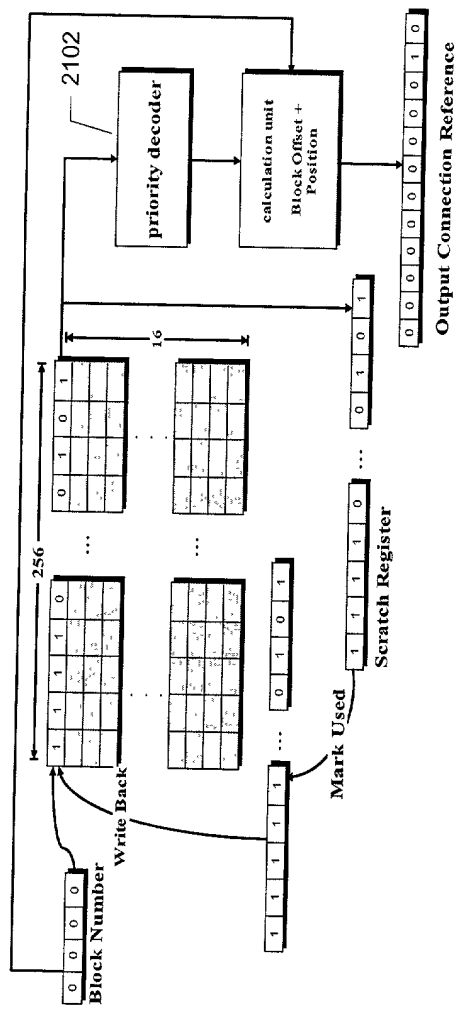


FIGURE 21

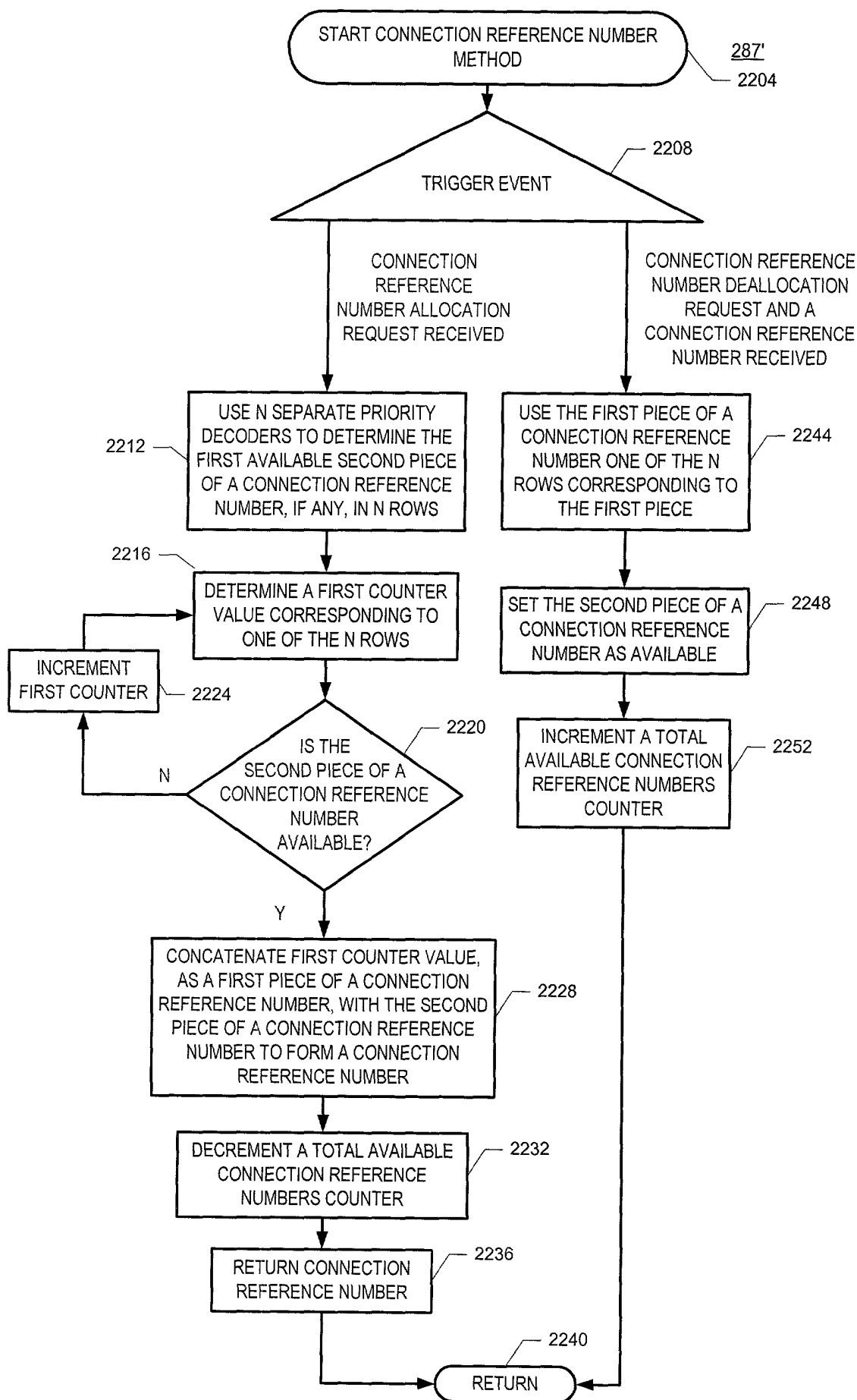
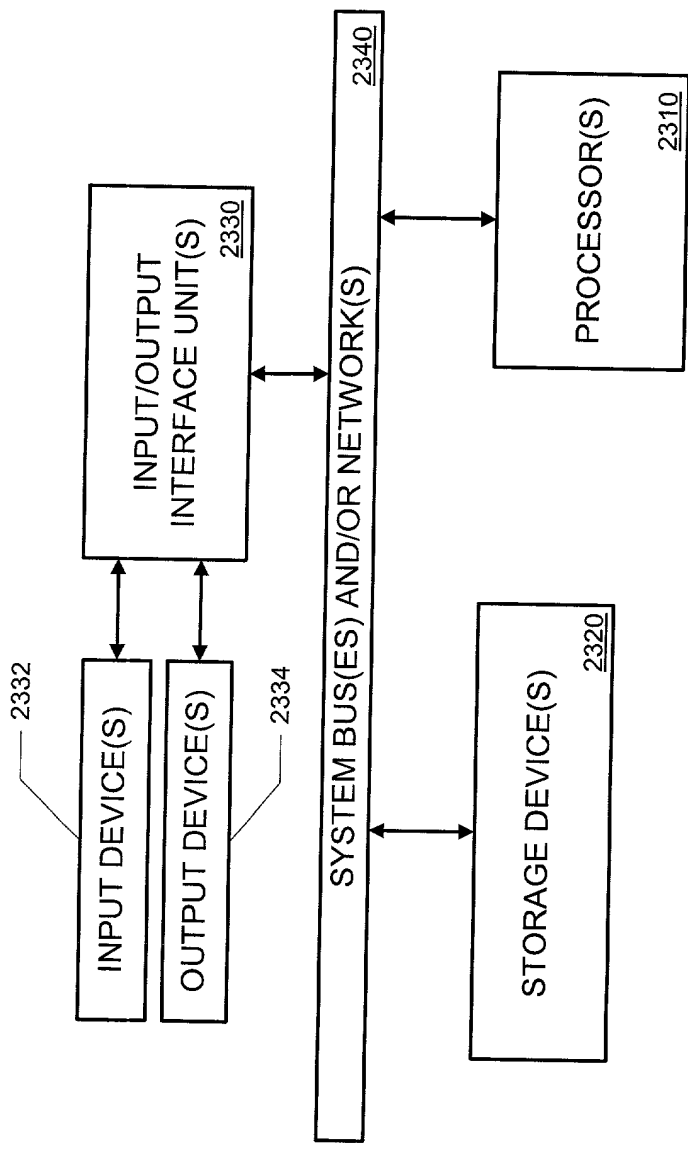


FIGURE 22





2300

FIGURE 23

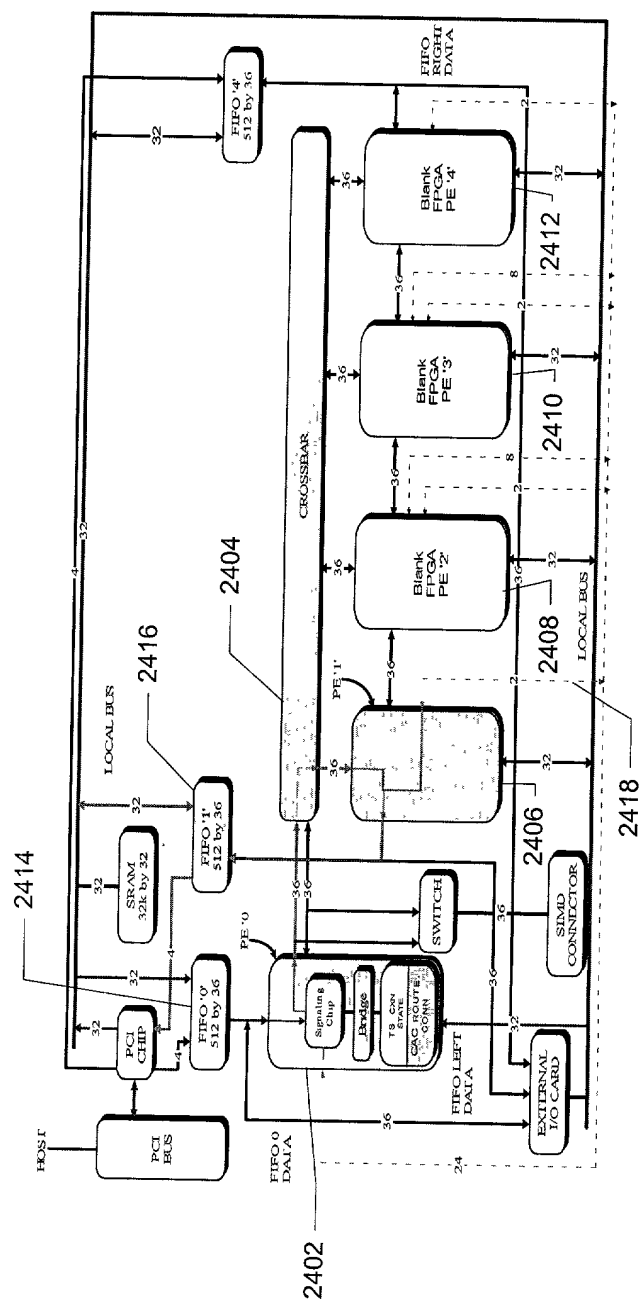


FIGURE 24

23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0								
CONNECTIVITY TABLE												ROUTING TABLE																			
PREVIOUS NODE ADDRESS												INTERFACE #				DEST. NODE ADDRESS				BANDWIDTH				OPT				NEXT NODE ADDRESS			
6-BIT												4-BIT				5-BIT				4-BIT				1-B				5-BIT			

FIGURE 28

INDEX		RETURN / WRITTEN VALUE					
OWN CONNECTION REFERENCE 5-BIT	CONNECTION REFERENCE 5-BIT (EACH)		STATE 2-BIT	BANDWIDTH 4-BIT	NODE ADDRESS 5-BIT (EACH)		OWN CONNECTION REFERENCE 5-BIT
	PREVIOUS	NEXT			Previous	NEXT	

2800

FIGURE 29

INDEX		RETURN / WRITTEN VALUE			
OWN CONNECTION REFERENCE 5-BIT	SEQUENTIAL OFFSET ( 0 to BW-1 ) 4-BIT	INCOMING CHANNEL IDENTIFIER 8-BIT		OUTGOING CHANNEL IDENTIFIER 8-BIT	
		INTERFACE	TIME-SLOT	INTERFACE	TIME-SLOT

2900

Table 1: Chip Select and Write Enable

CS_N	WE_N	ACTION
0	0	Write
0	1	Read
1	0	Pause
1	1	No action/High Impedance

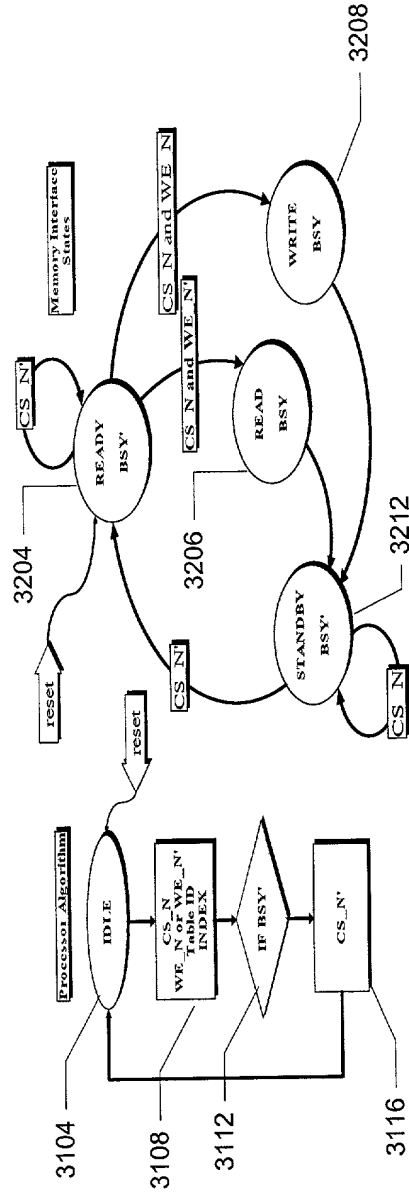


FIGURE 31

FIGURE 32



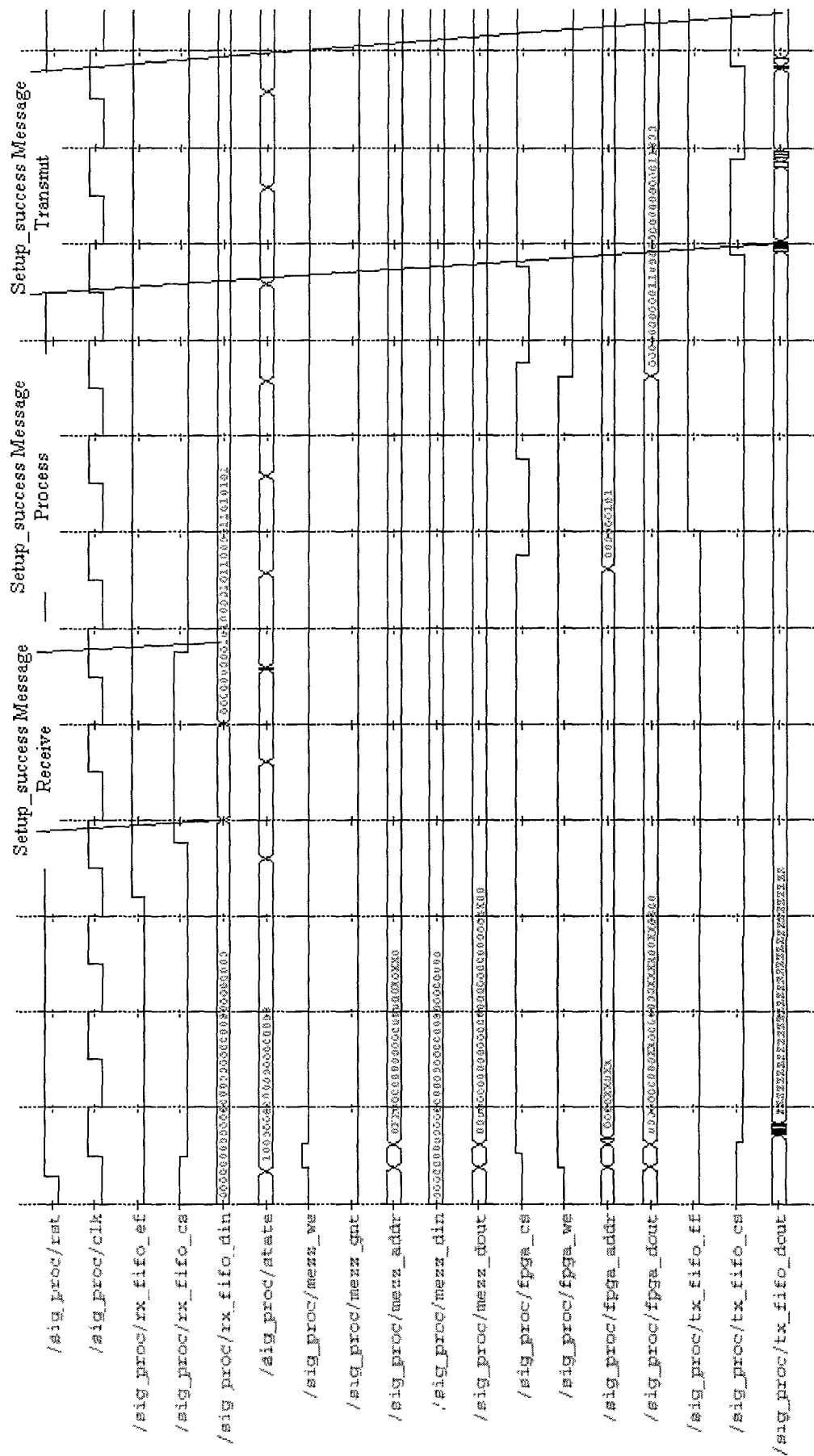


FIGURE 34



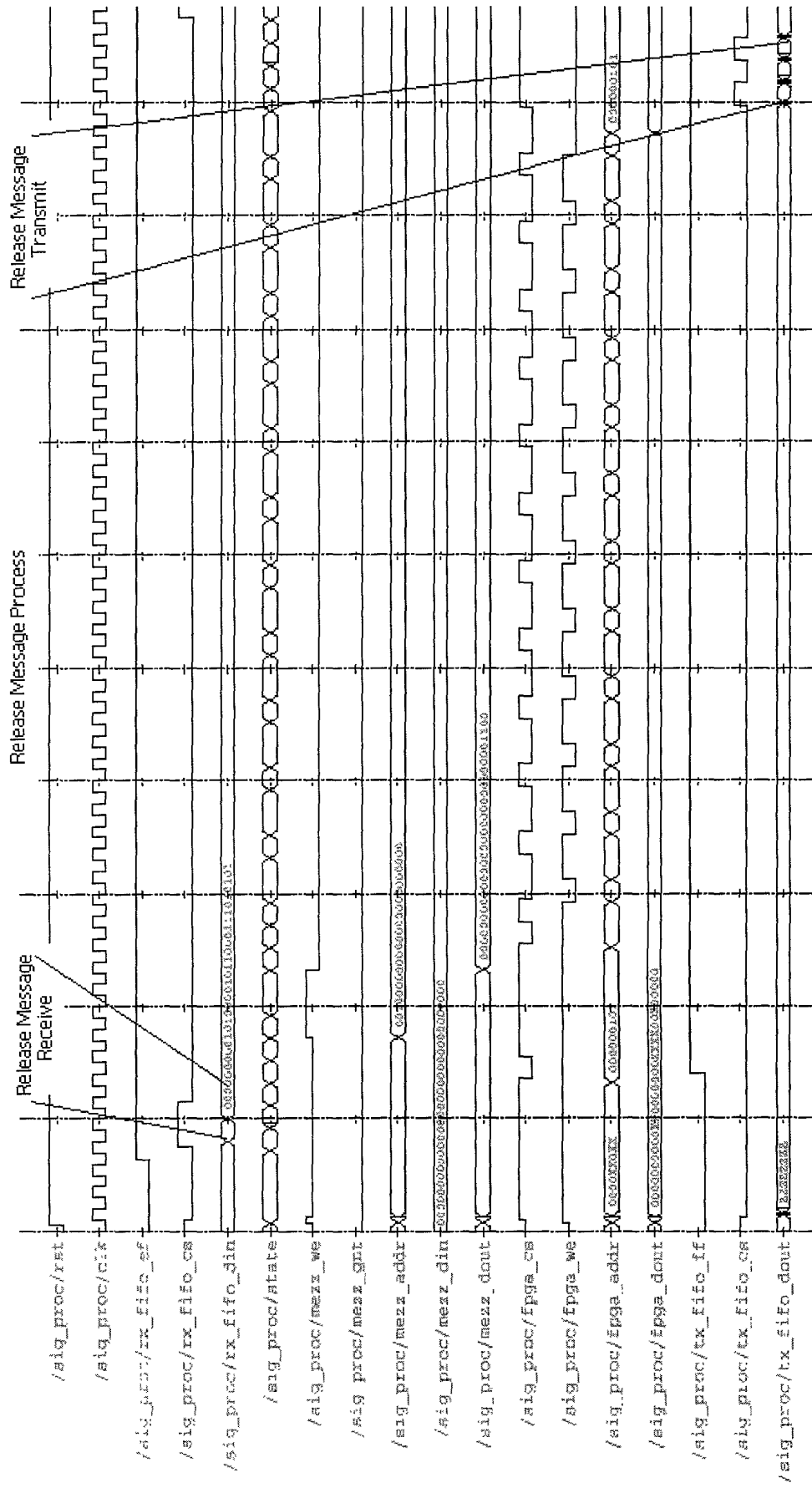


FIGURE 35

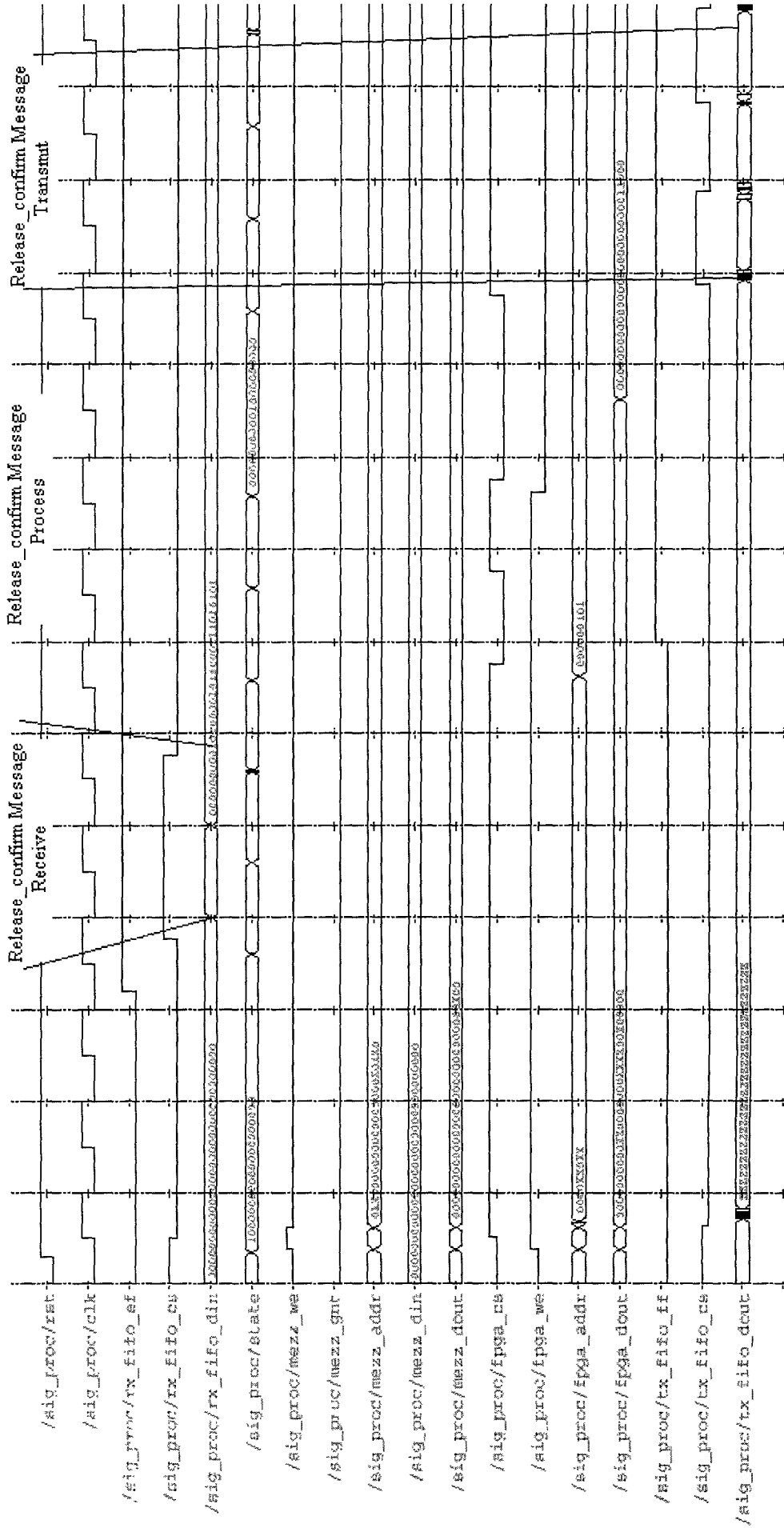


FIGURE 36

Type	Operations and Cycles										Total
Setup	Op	RX	RD route & CAC	WR CAC	RD Conn	RD Cxn ref	RD TS & WR switch	WR State	TX		77-101*
	cycles	12	8-32	2	4	2	36	1	12		
Setup success	Op	RX	RD State					WR State	TX		9
	cycles	3	2					1	3		
Release	Op	RX	RD State	RD CAC & WR CAC	RD Switch & WR TS			WR State	TX		51
	cycles	3	2	6	36			1	3		
Release confirm	Op	RX	RD State	WR Cxn ref							10
	cycles	3	2								

FIGURE 37